

Electronic Acknowledgement Receipt

EFS ID:	3816472
Application Number:	10699707
International Application Number:	
Confirmation Number:	3525
Title of Invention:	Reconfigurable chip level equalizer architecture
First Named Inventor/Applicant Name:	Antonio F. Mondragon-Torres
Customer Number:	23494
Filer:	Utpal Dilipkumar Shah/Jennifer Ringer
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Attorney Docket Number:	TI-35731
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1		06700ROAMAILEDMAY292008.pdf	148223 c678683fd24b627bfbd40153e3c7108eeda e4f0	yes	10

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Amendment - After Non-Final Rejection	1	1
	Claims	2	6
	Applicant Arguments/Remarks Made in an Amendment	7	10

Warnings:

Information:

Total Files Size (in bytes):	148223
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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